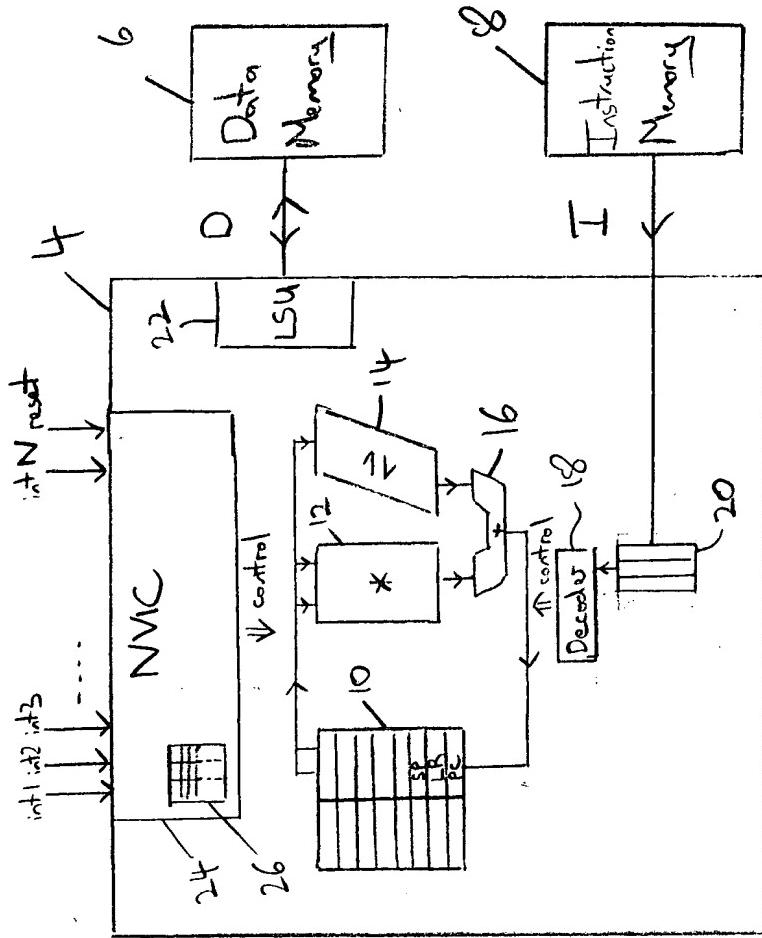




2



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NVIC Control Table

	Pending	Active	Priority	
			pre-emption	ordering
Int 0		1		↔
Int 1				↓
Int 2	1			↓
Int 3		1		↓
⋮				

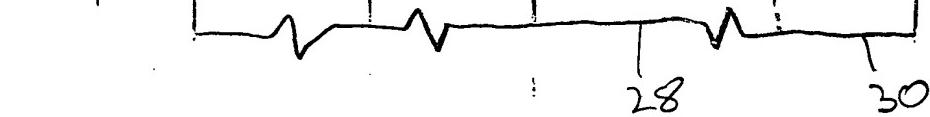
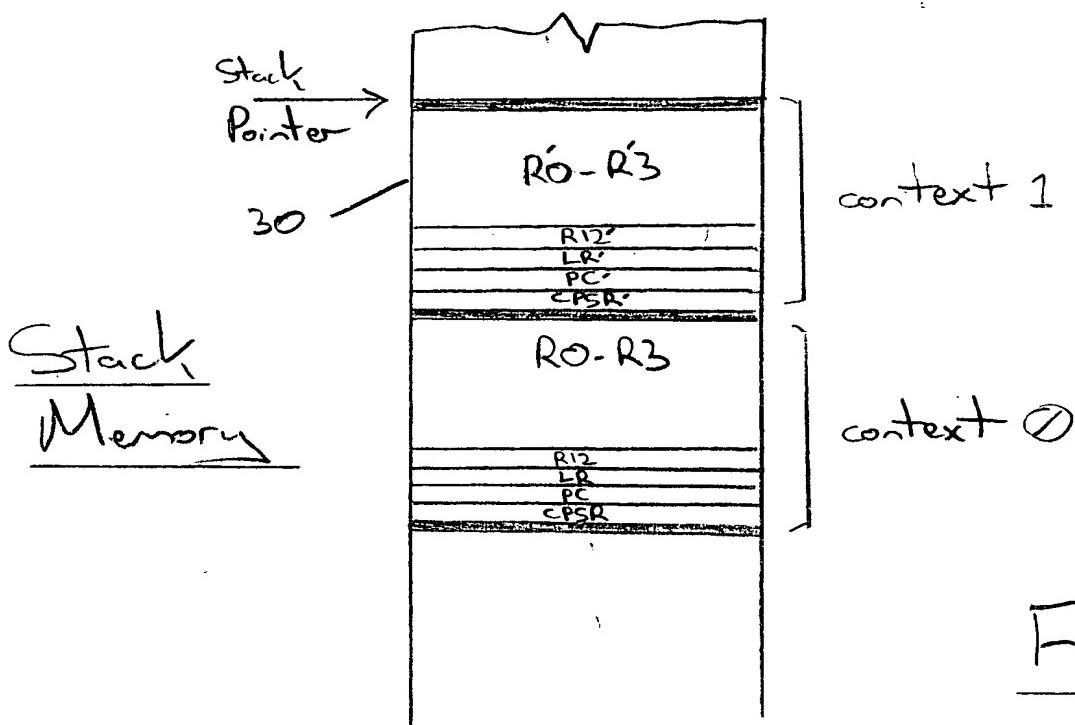


Fig. 2



Priority Inversions

32

Int A	(priority 2)
Int B	(priority 3)
Int C	(priority 7)

Int A
priority
change

Int A	(priority 5)
Int B	(priority 3)
Int C	(priority 7)

Stack priority = 3

Int C
priority
change

Int A	(priority 2)
Int B	(priority 3)
Int C	(priority 1)

Stack priority = 1

Fig. 4

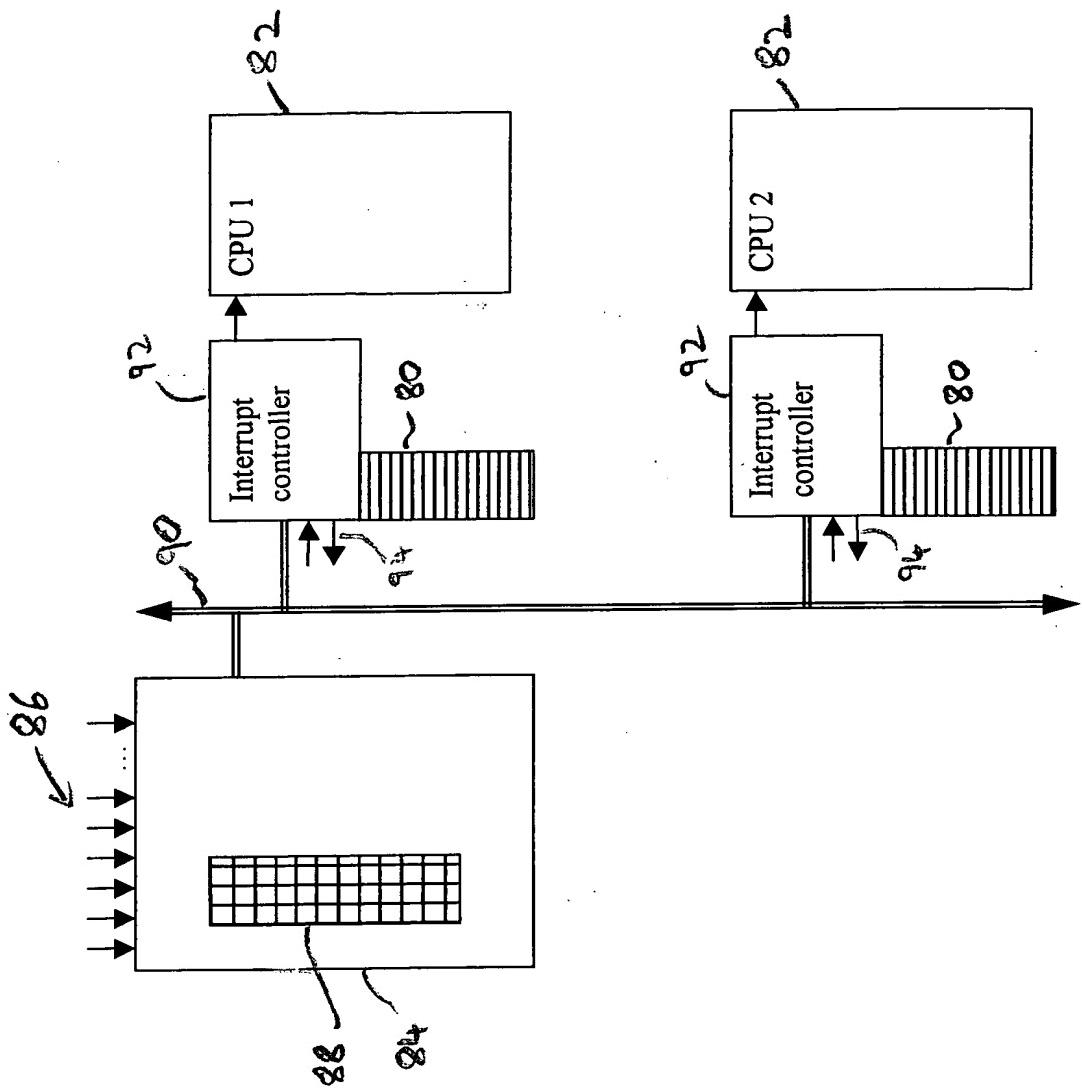


Fig. 5

Main Flow

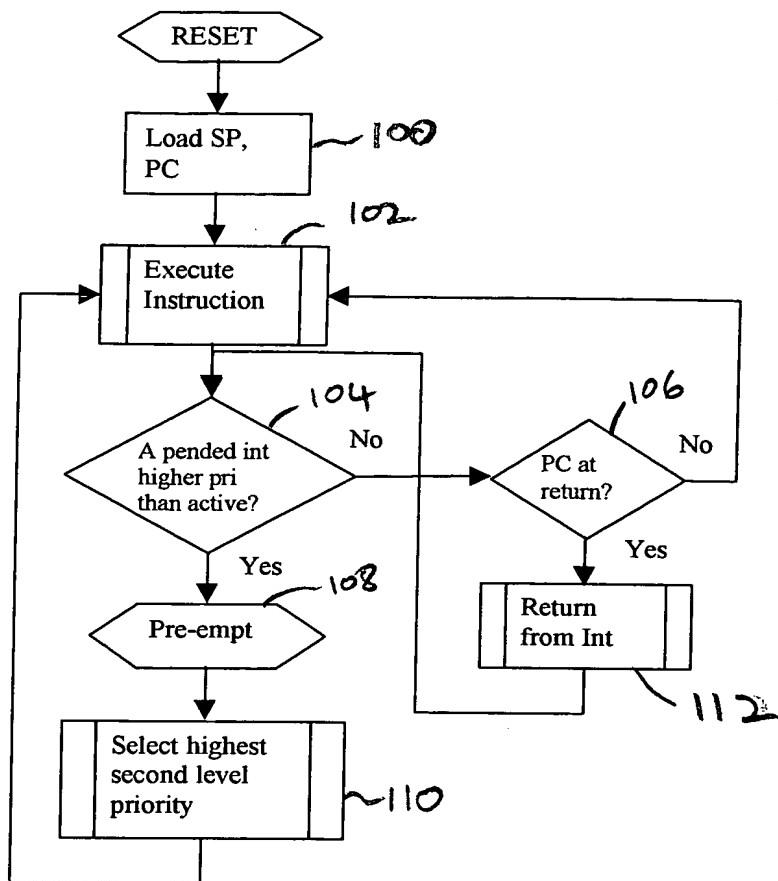


Fig. 6

Main Flow with Tail Chaining

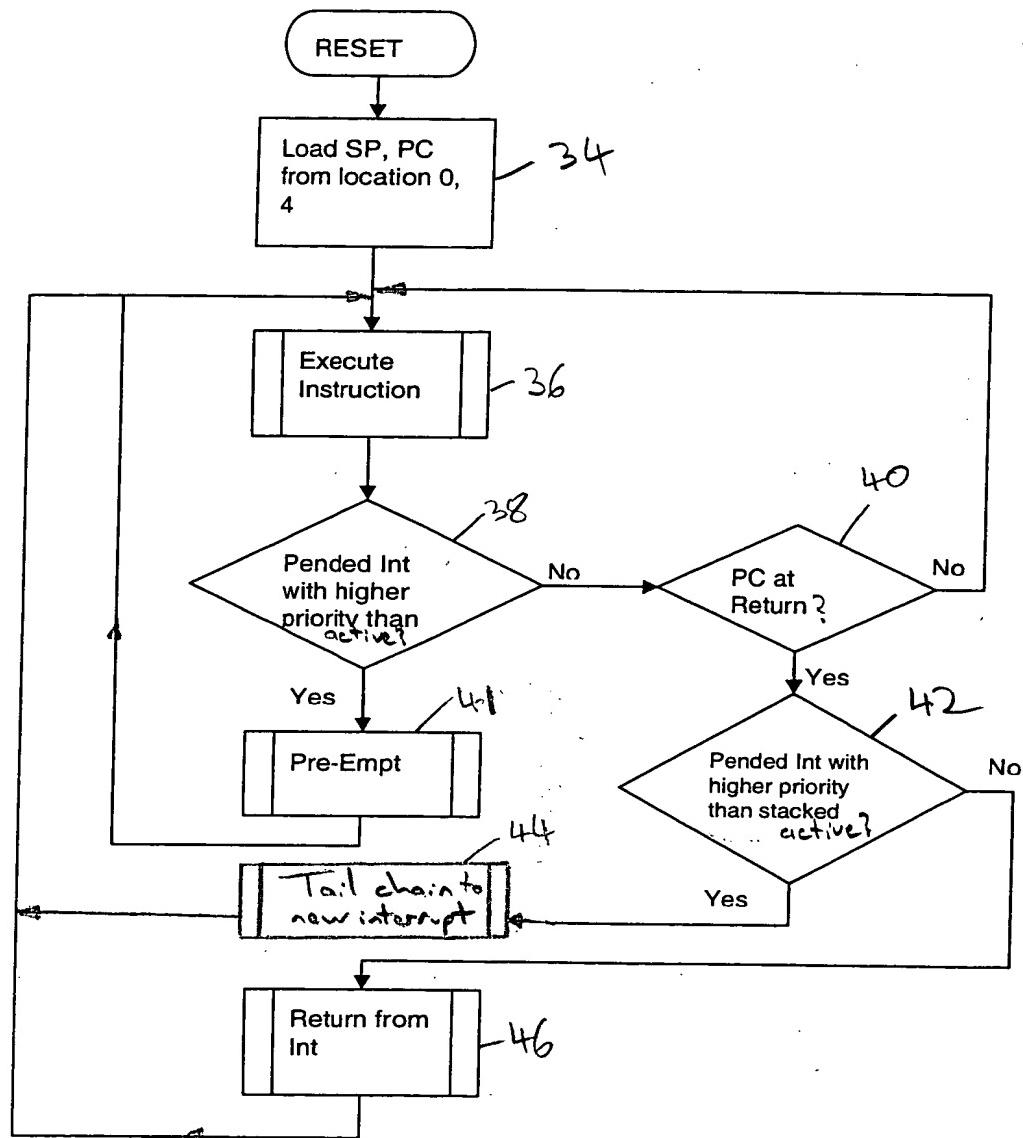


Fig. 7

Pre-emption Flow

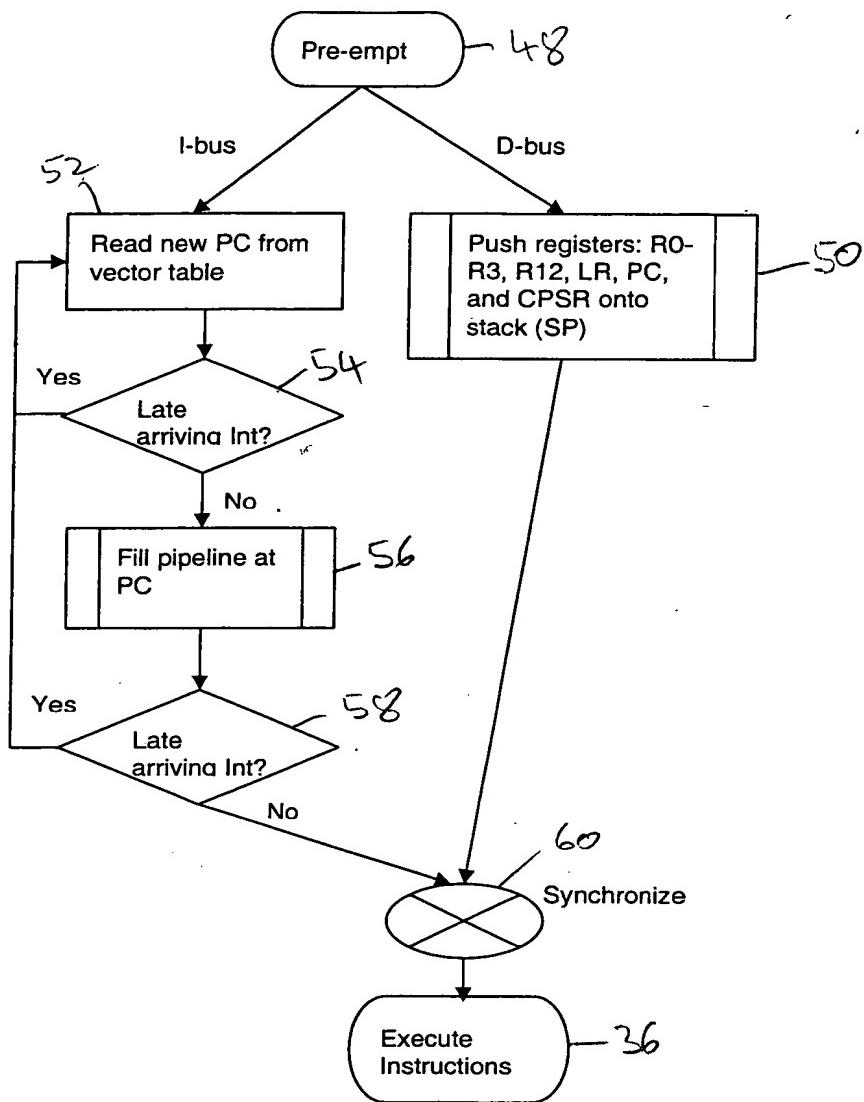


Fig. 8

Return From Interrupt Flow

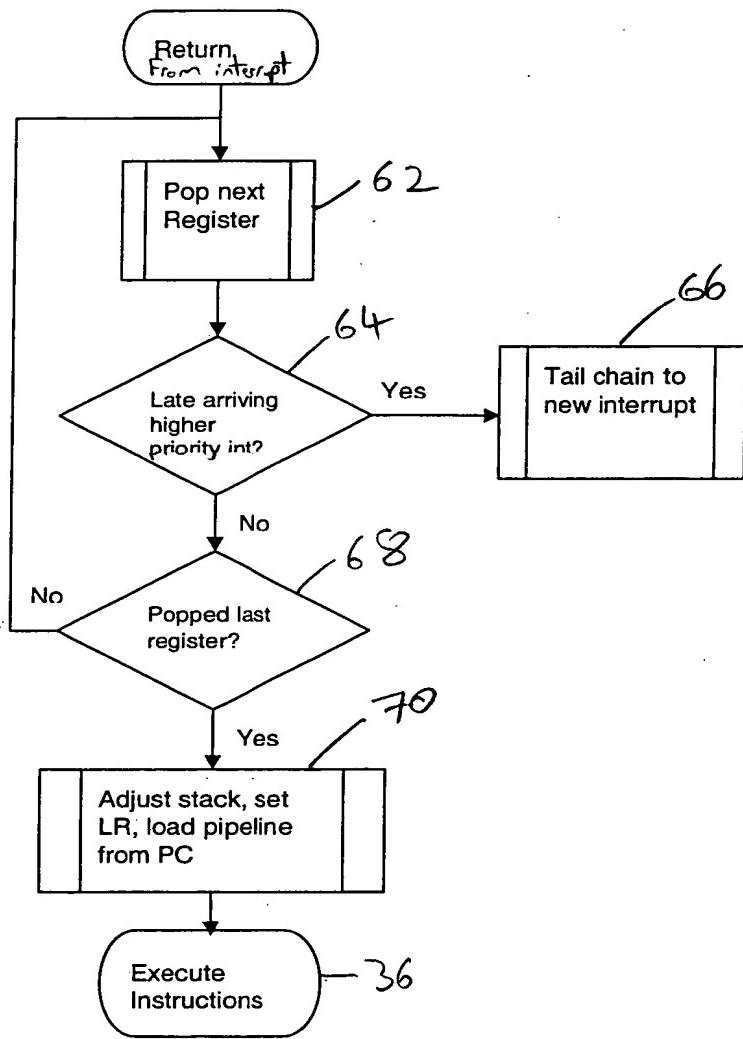


Fig. 9

Tail Chaining Flow

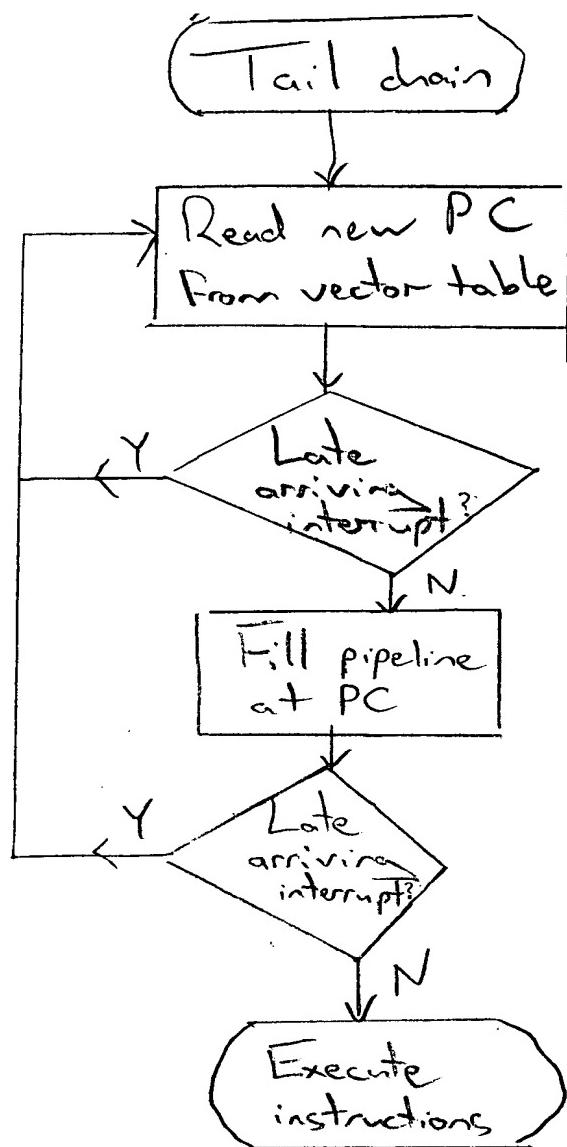


Fig. 10